

JUN 21 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

App. Serial No. : 10/044,273
 Applicant : William John Saiki et al.
 Filed : January 10, 2002
 Art Unit : 2816
 Examiner : Zweizig, Jeffery Shawn

Atty. Docket No. : 2102397-991180
 Customer No. : 26379

**Title: HIGH VOLTAGE GENERATION AND REGULATION SYSTEM FOR DIGITAL
MULTILEVEL NONVOLATILE MEMORY**

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June 21, 2004Rosa A. Caviedes
Rosa A. Caviedes

* * *

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement mailed on May 28, 2004, Applicant hereby elects to prosecute Group I, Claims 1-7, 9, 11-31, 41-48 and 62-82 without traversal.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 20 of this paper.

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Commissioner for Patents
Alexandria, VA 22313-1450Telephone:

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From: Edward B. Weller
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Client-Matter Number: 2102397-991180

Ed B. Weller

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Attorney Docket No. 2102397-991180

U.S. Serial No.: 10/044,273

Filing Date: January 10, 2002

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MULTILEVEL NONVOLATILE MEMORY

Enclosed is:

1. Response to Restriction Requirement.

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